L Number	Hits Search Text DB Time stamp 2181 computer mother adj board memory DRAM\$ USPAT; 2003/10/18 09:29
1	SRAM\$1 ROM\$1) same GHz US-PGPUB;
,	EPO; JPO
; 2. T	495 ((computer mother ad) board memory DRAM\$1 USPAT; 2003/10/18 09:32
!	SRAM\$1 ROM\$1) same GHz) and (signal\$1 US-PGPUB;
. 1	power ground) adj5 (line\$1 wire\$1 wiring\$1 EPO; JPO
1	bus\$2) same (interconnect\$4 connect\$4)
1 3	45 (((computer mother ad) board memory USPAT; 2003/10/18 09:33;
	DRAM\$1 SRAM\$1 ROM\$1) same GHz) and US-PGPUB;
1	(signal\$1 power ground) adj5 (linc\$1 EPO; JPO
1	wire\$1 wiring\$1 bus\$2) same
1	(interconnect\$4 connect\$4)) and coaxial
4	near7 (line\$1 wire\$1 wiring\$1 bus\$2) same
	(interconnect \$4 connect \$4)